# S.No. 18012

# MCA, Semester II Examinations, May 2018 Computer Networks and Data Communication Mark

Time:	3 Hours Marks:80
Note:	Attempt five questions in all, selecting one question from each
	of the four units. Q. No. 1 is compulsory.
Q. 1	Explain following: 8x3=24
i.	Topology
ii.	ATM
iii.	Bandwidth
iv.	Communication Mode
v.	GSM
vi.	Error Conrol
vii.	Flooding
viii.	Load Shedding
	UNIT-I
Q. 2	Explain functions and design issues of each layer of OSI Reference Model.
Q. 3	(a) Define computer network. Explain categorization of network with suitable examples.
	(b) State and explain the concept of Frame relay. 7
	UNIT-II
Q. 4	Explain Data communication model. Discuss various types of guided transmission media and wireless media. 14
Q. 5	(a) Compare FDM and TDM 7
	(b) Compare Circuti switching and packet switching.
	UNIT-III
Q. 6	Explain various design issues of Data Link Layer. Discuss various types of protocols at DLL with their specifications.
Q. 7	Make a comparison among IEEE 802.3, IEEE 802.4 and IEEE 802.5.
	UNIT-IV
Q. 8	What is Routing? Explain various types of routing
Q. 6	algorithms. Discuss the principles of routing in Adhoc networks.
Q. 9	(a) Explain various design issues of network layer. 7 (b) Explain various congestion control algorithms. 7

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MCA, Second Semester, M	Tay 2018
Object Oriented Modelling & Progar	nming using c.
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	all, selecting one
question from each of the four	r units. Q. No. 1 18
compulsory.	8x3=24
Q. 1 Explain following:	ONO 2
i. Deployment diagram	
ii. Hierarchy	
iii. Structure in C++	
iv. Object initialization	
v. Overriding	
vi. Abstract class	
vii. Vector	
viii. Containers	
UNIT-I	- 1: it- vorious
Q. 2 What is object orientation?	Explain its various
characteristics? Discuss in	brief object offerted
dovolonment life cycle	
a a Dalain the role of mod	eling with suitable
example. Discuss various cor	nponents of USE-case
and sequence diagram.	1.1
UNIT-II	
Q. 4 What is a function? Explai	n the delinition and
syntax of a function. Write a	program to illustrate
of requirement function.	11
0.5 (a) What is a constructor? Ex	xplain various types of
and the store with programm	ing example.
as position with suitable	example the public,
private and protected deriva-	tions. Also discuss the
concept of virtual base class	<b>.</b>
	P.T.O.

# UNIT-III

Q. 6 Q. 7	What is an Exception? Explain the process of handling exceptions and re-throwing at exception. Give suitable example.  (a) Compare overloading and overriding	11
Q. 1	(b) Explain new and delete operators	5
	(c) What is polymorphism? Explain various type	es.
	of polymorphism.	5
	UNIT-IV	
Q. 8	What is a template? Discuss in brief various type	es
Q. 0	of templates. Explain the process of overloading	of 4
	template.	7
Q. 9	(a) Discuss various components of 512.	.00
	(b) Explain the process of error handling during file operations.	ng 7

Time: 3 Hours  Note: Attempt any five any	Con	A			allull, [v]	lay 2018	
		iputer O	riented (	Optimiz	ation Te	chniques	
The dily live que	estions, select	ina one au	action from			n No. 1 is compulso	Max. Marks: 80
<ol> <li>Explain the following</li> <li>a) What are the main period</li> <li>b) Explain the role of section</li> <li>c) Name three method</li> <li>d) What is traveling section</li> </ol>	phases of ope ensivity analy s of finding a	ration res /sis in dec	earch stud	y?			8x3=24
e) What is two person f) What are different p g) Write down some in h) Define Simulation, as	zero sum gan roperties of a	ne? dynamic	programn	ning prob	lem?		
			TT				
scope of Operation R	lesearch in bu	siness an	d Industry	in charac	teristics f	features. Also give	examples to highlights th
b) what is role of Decis	ion Making in	OD Eval	aim it.				7 7
3. Solve the following Linear Maximize $Z = 5x_1$	$+7x_2$ Subject	g Prblem	by graphic	al metho	d:		14
$   \begin{array}{c}     x_1 + x_2 \le 4 \\     3x_1 + 8x_2 \le \\     10x_1 + 7x_2 \le 4   \end{array} $	24		mstraints				
$x_1, x_2 \geq 0$							
4. Consider the transportation	n problem ha	ving the f	Un	it – II			
3 4 4		wing the f	onowing p	arametei	table:		14
		A	В	С	D	Supply	
	I	3	5	2	4	5 5	
	II	4	6	6	1	6	
	III	3	1	12	1	7	
	IV	8	4	5	. 3	4	
	Demand	3	- 5	4	5		
Han Vogel's							
Use Vogel's approxima 5. Consider the problem of a Operator 1 cannot be assignement using the	assigned to	machine	rs to four	machine	nd then so s. The ass can not b	olve the problem. signement cost in oe assigned to mad	thine 4. Find the optimal
Operator 1 cannot be	assigned to	machine	rs to four	machine	s. The ass	olve the problem. signement cost in oe assigned to mad	Rupees are given below. Thine 4. Find the optimal 14
Operator 1 cannot be	assigned to	machine	rs to four	machine ertator 3 ".	s. The ass	olve the problem. signement cost in pe assigned to mad	thine 4. Find the optimal
Operator 1 cannot be	assigned to	machine Assignmer	3 and opent method	machine ertator 3	s. The ass	signement cost in oe assigned to mad	thine 4. Find the optimal
Operator 1 cannot be	assigned to	machine Assignmer	3 and opent method	machine ertator 3	s. The ass	signement cost in oe assigned to mad	thine 4. Find the optimal
Operator 1 cannot be	assigned to	machine Assignmer	3 and opent method	machine ertator 3	s. The ass	signement cost in pe assigned to made	thine 4. Find the optimal

# Examination MCA 2<sup>nd</sup> Sem. May-2018 Subject - Advanced Computer Architecture Paper: MCA-MT17-22

Time: 3 hrs

Max. Marks: 80

Note: Question number 1 is Compulsory (24 marks). Attempt five questions in all by selecting at least one question from each unit (14 marks). All questions carry equal marks. O1.Explain the following:

- a) Computational Model
- b) Branch Problem
- c) single shared buses
- d) Pended & split transaction buses
- e) hierarchical cache coherence protocols
- f) Pipelined Processing of integer
- g) COMA models
- h) Superscalar processors

### Unit-1

- Q2.a). What is Instruction level parallel (ILP) Processors? Explain the Code-Scheduling for ILP-Processors in detail.
- b). Explain in detail the evolution and interpretation of computer architecture?
- Q3 a). Explain in detail the relations between programming languages and parallel architectures.
- b). What is parallel processing? Explain the Types and levels of parallelism in detail.

## Unit-2

Q4. Explain the following in detail:

- a) Branch penalties and schemes to reduce them
- b) Delayed branching and branch detection and prediction schemes
- Q5. What is Superscalar Processors? Explain the different tasks of superscalar processing in detail.

#### Unit-3

- Q6. What is MIMD Architectures? Explain the concepts of distributed and shared memory MIMD architectures in detail.
- Q7 a). What is Direct Interconnection Networks? Explain in detail
- b). Explain the following in detail:
  - i. 2D mesh and Barrel shifter
  - ii. Linear array and Hypercubes

#### Unit-4

Q8. What is Cache coherence problem? Explain Snoopy cache protocol, Directory schemes and software based protocols in detail.

Q9. Explain the following in detail:

- a) Comparison of bandwidths of locked and Crossbar
- b) Multistage Networks